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Design of multilevel mixed-mode simulator for LSI/VLSI circuits

J Dabrowski - Circuits and Systems, 1988., IEEE International Symposium on, 1988 - ieeexplore.ieee.org

... While the Mrls can be either of lo6tc type CL) or circuit for CC each **node N** in
the graph) do C if C there is at least ... **Four-state simulation** logic is used. ...

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Finite discrete Markov process clustering - group of 7 »

G Ridgeway - 1997 - dttx.citd.edu.vn

... 2.5 Simulation Experiment I performed a two cluster/**four state simulation** experiment.

I generated 4829 processes from P 1 and 171 processes from P 2

Cited by 14 - Related Articles - [View as HTML](#) - [Web Search](#)

A Smart Monte Carlo Technique for Free Energy Simulations of Multiconformational Molecules. Direct ... - group of 2 »

H Senderowitz, F Guarnieri, WC Still - Journal of the American Chemical Society, 1995 - pubs.acs.org

Page 1. J. Am. Chem. SOC. 1995, 117, 8211-8219 821 1 A Smart Monte Carlo Technique

for Free Energy Simulations of Multiconformational Molecules. ...

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A logic simulation engine based on a modified data flowarchitecture - group of 3 »

A Mahmood, WI Baker, J Herath, A Jayasumana - Computer-Aided Design, 1992. ICCAD-92. Digest of Technical ... , 1992 - ieeexplore.ieee.org

... For a four input gate with **four state simulation** (hih, low, undefined, or high impedance), a total of 44 = 256 entries are required in the lookup table. ...

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Design of multilevel mixed-mode simulator for LSI/VLSI circuits

J Dabrowski - Circuits and Systems, 1988., IEEE International Symposium on, 1988 - ieeexplore.ieee.org

... with proper delays instead of differential equations. **Four-state simulation**

logic is used. The interfacing between C— and L—type ...

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The Dangers of Living with an X (bugs hidden in your Verilog) - group of 2 »

M Turpin - Boston SNUG, 2003 - averant.com

Page 1. The Dangers of Living with an X (bugs hidden in your Verilog) Version 1.1

(14 th October, 2003) Mike Turpin Principal Verification Engineer ...

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Key: IEEE JNL = IEEE Journal or Magazine, IEE JNL = IEE Journal or Magazine, IEEE CNF = IEEE Conference, IEE CNF = IEE Conference, IEEE STD = IEEE Standard

1. Simulation of magnetization switching in biaxial single-domain ferromagnetic particles

Xuekun Kou; Novotny, M.A.; Rikvold, P.A.;
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Latkoski, P.; Hadzi-Velkov, Z.; Popovski, B.;
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Volume 1, 28-30 Sept. 2005 Page(s):97 - 100 vol. 1

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Al-Aomar, R.;
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